PRINTER RUSH
(PTO ASSISTANCE)

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[RUSH] **MESSAGE:** Please supply missing Serial No. on page 15, line 12.

Thank you

[XRUSH] **RESPONSE:** Corrected

Kenneth Wright - 408-749-6900 **INITIALS:** KB

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Alternatively, the HID can comprise a single chip implementation as illustrated in Figure 8. The single chip includes the necessary processing capability implemented via CPU 801 and graphics renderer 805. Chip memory 807 is provided, along with video controller/interface 806. A internal bus (USB) controller 802 is provided to permit communication to a mouse, keyboard and other local devices attached to the HID. A sound controller 803 and interconnect interface 804 are also provided. The video interface shares memory 807 with the CPU 801 and graphics renderer 805. The software used in this embodiment may reside locally in on-volatile memory or it can be loaded through the interconnection interface when the device is powered.

The operation of the virtual desktop system architecture is described in co-pending U.S. Patent Application serial number 09/063,335, filed April 20, 1998, entitled "Method and Apparatus for Providing A Virtual Desktop System Architecture" and assigned to the present assignee, and incorporated herein by reference.

Authentication and Session Managers

Within the virtual desktop system architecture (and others), an Authentication Manager is responsible for receiving information from the HIDs, including the status of any identification token presented by the user (e.g., a smart card). The Authentication Manager determines if the user is to be allowed to access a computational service, and if so determines the computational server that should provide the service. In addition, it can select one of a set of session types that will be presented at the HID. For example, a user that provided a smart card at the HID may be allowed to access more services than one that did not.